REPLACEMENT SHEET

Title: HIGH SPEED DIGITAL PATH FOR SUCCESSIVE APPROXIMATION
ANALOG-TO-DIGITAL CONVERTERS
1st Named Inventor: Chad Thomas Steward JAN 0 3 2006 Docket No.: 55123P298 Application No.: 10/812,242 Sheet: 1/11 P. TANKEN 4 4 Φ LATCH ₽ ₽ -11 SWITCHED CAPACITOR DAC ₽ STATE MACHINE SUCCESSIVE APPROXIMATION FIG. 1 (Prior Art) REGISTER ₽ **₽ ₽** -OH **-₽** ₽ **₽** ₽ -01 -10-4 4

Title: High Speed Digital Path for Successive Approximation Analog-to-Digital Converters

1st Named Inventor: Chad Thomas Steward

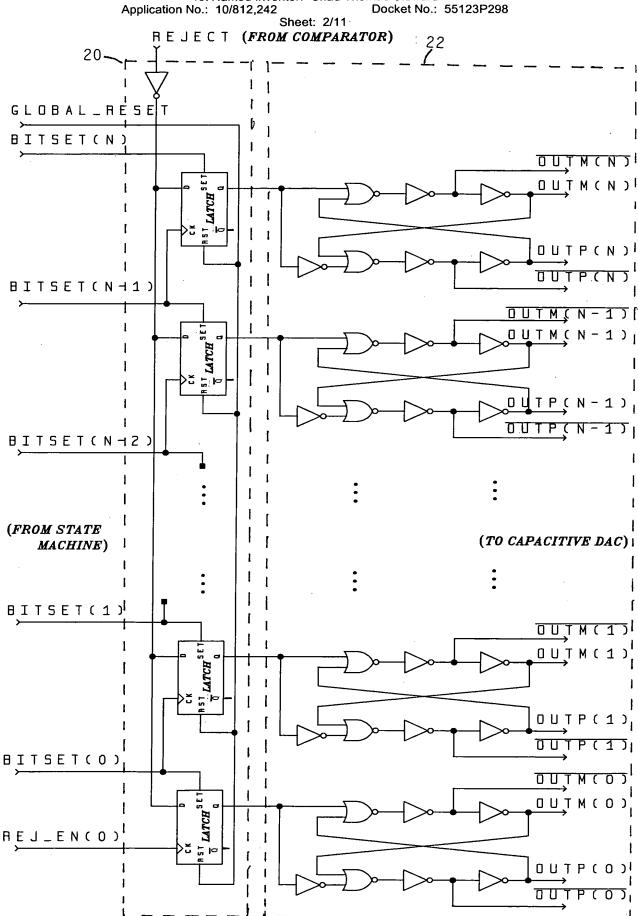
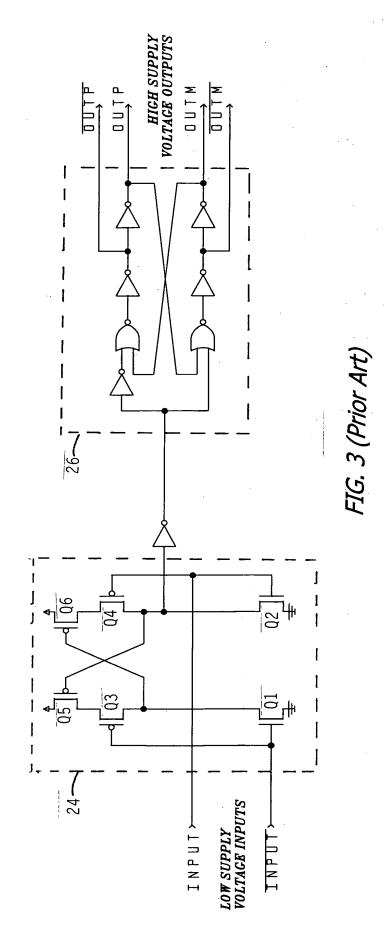


FIG. 2 (Prior Art)

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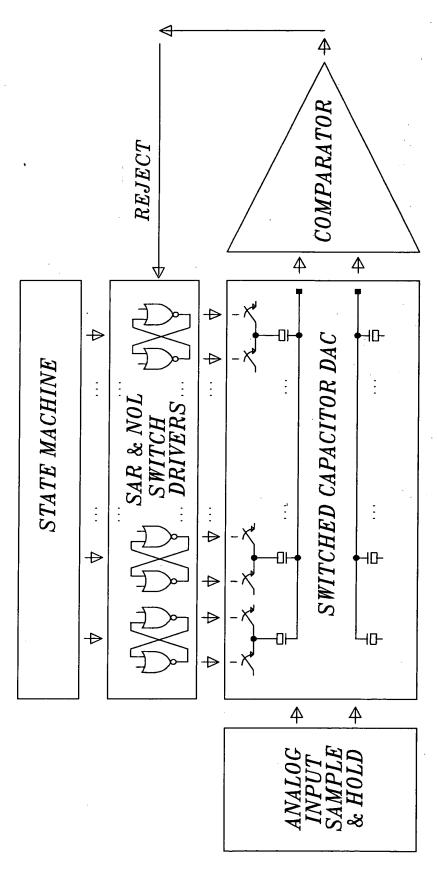
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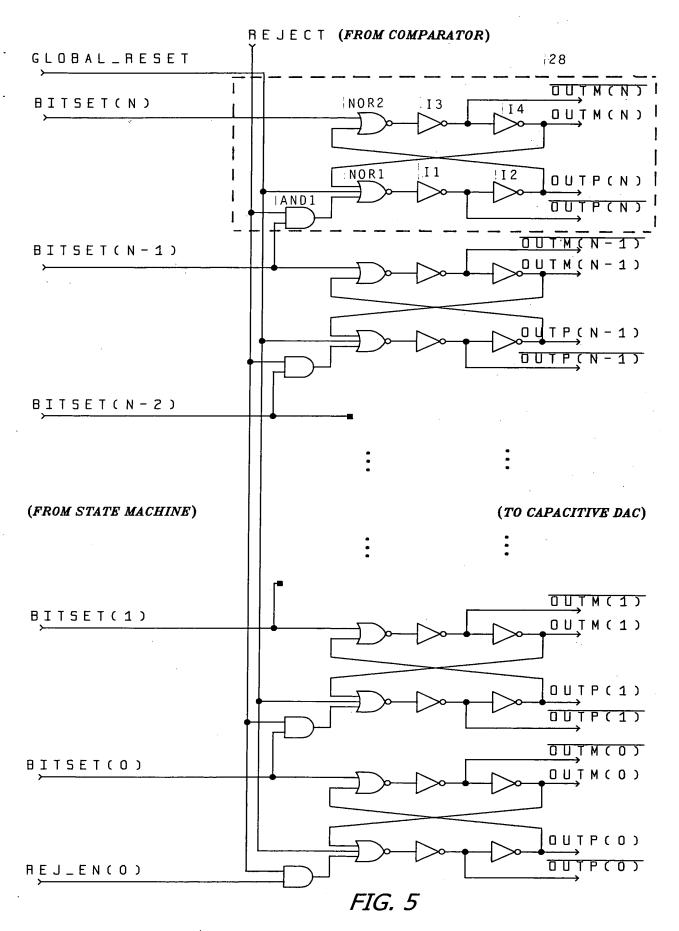


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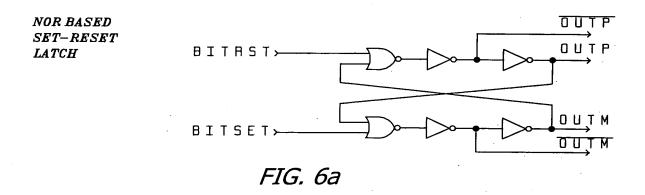


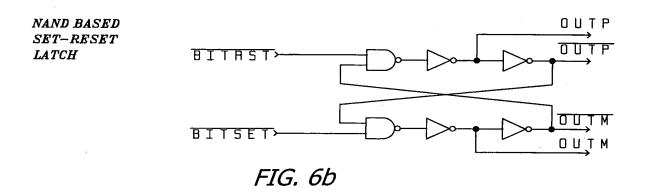
Title: HIGH SPEED DIGITAL PATH FOR SUCCESSIVE APPROXIMATION ANALOG-TO-DIGITAL CONVERTERS

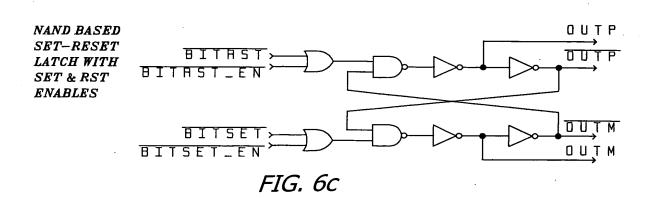
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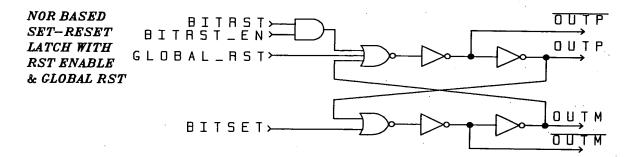


FIG. 6d

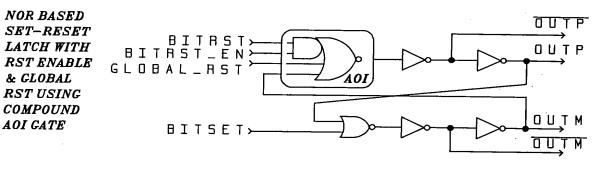


FIG. 6e

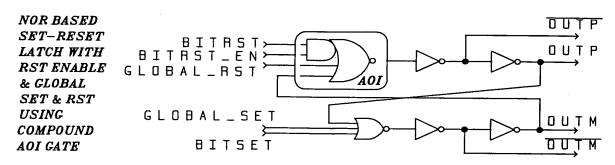


FIG. 6f

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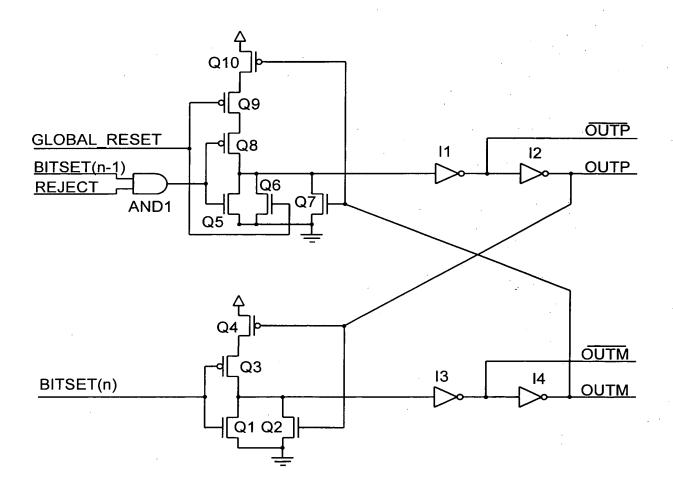


FIG. 7

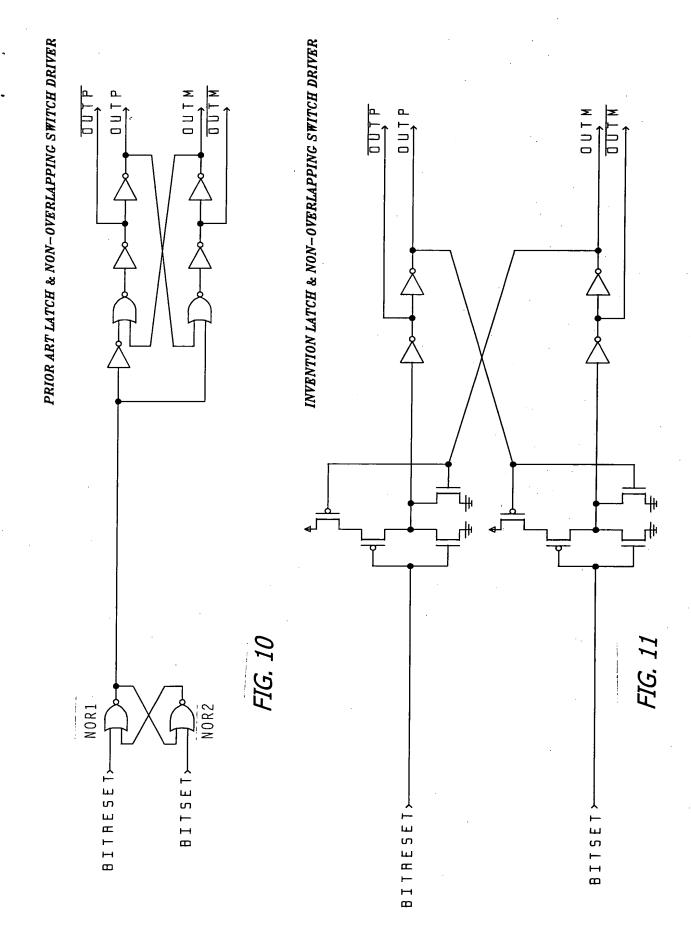
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1st Named Inventor: Chad Thomas Steward
Application No.: 10/812,242 Docket No.: 55123P298 Sheet: 9/11 PRIOR ART LEVEL-SHIFTER & NON-OVERLAPPING SWITCH DRIVER HIGH SUPPLY VOLTACE OUTPUTS INVENTION LEVEL-SHIFTER & NON-OVERLAPPING SWITCH DRIVER HICH SUPPLY VOLTACE OUTPUTS OUTP O U T M LOW SUPPLY VOLTACE INPUTS LOW SUPPLY VOLTACE INPUTS INPUT INPUT

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